## AMENDMENTS TO THE CLAIMS

Please add or amend the claims to read as follows, and cancel without prejudice or disclaimer to resubmission in a divisional or continuation application claims indicated as cancelled:

1.  (previously presented) A method for operating a non-volatile memory device comprising: using one or more unused bits of an address argument of a command as an addressing mode field to determine whether said address argument is a byte address argument or a block address argument.

2.  (original) The method of claim 1 comprising: determining that the address argument is the byte address argument when the addressing mode field is zero.

3.  (original) The method of claim 1 comprising: determining that the address argument is the block address argument when the addressing mode field is one.

4.  (original) The method of claim 2 further comprising: accessing a byte address within a memory unit according to the byte address argument if said address argument is a byte address argument.

5.  (original) The method of claim 3 further comprising: accessing a block address within a memory unit according to the block address argument if said address argument is a block address argument.

6.  (original) The method of claim 1, wherein using said one or more unused bits comprises using a least significant bit of said address argument.

7.  (original) The method of claim 1, wherein using said one or more unused bits comprises using a most significant bit of said address argument.

8.  (Currently amended) An apparatus comprising: a non-volatile memory unit; and a controller [to] adapted to use one or more unused bits of an address argument of a command as an addressing mode field to determine whether an addressing mode to access said memory unit is a byte addressing mode

or a block addressing mode and to send a command to access data within said memory unit according to said addressing mode.

9. (original) The apparatus of claim 8, wherein said memory unit is a multi media card (MMC).

10. (original) The apparatus of claim 8, wherein said memory unit is a secure digital (SD) memory card.

11. (original) The apparatus of claim 8, wherein the addressing mode is associated with the ninth bit of a 48-bit command having a 32-bit address argument.

12. (Currently amended) The apparatus of claim 8, wherein the addressing mode is associated with the [31-st] 40-th bit of a 48-bit command having a 32-bit address argument.

13. (Currently amended) a storage medium having stored thereon instructions that, when executed by a computing platform functionally associated with a non-volatile memory device, result in: [using an addressing mode field of an address argument of a command] using one or more unused bits of an address argument of a command as an addressing mode field to determine whether said address argument is a byte address argument or a block address argument.

14. (previously presented) The storage medium of claim 13 wherein the instructions, when executed result in: using one or more unused bits of the address argument as the addressing mode field.

15. (previously presented) The storage medium of claim 13, wherein the instructions when executed further result in: determining that the address argument is the byte address argument when the addressing mode field is zero.

16. (previously presented) The storage medium of claim 13, wherein the instructions when executed further result in: determining that the address

argument is the block address argument when the addressing mode field is one.